

BEST AVAILABLE COPY

WEST Search History

DATE: Friday, February 09, 2007

Hide?	Set Name	Query	Hit Count
	<i>DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L29	L21 and (PWM or (pulse width modulation))	3
<input type="checkbox"/>	L28	L20 and (PWM or (pulse width modulation))	31
	<i>DB=PGPB; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L27	L21 and (PWM or (pulse width modulation))	7
<input type="checkbox"/>	L26	L20 and (PWM or (pulse width modulation))	20
	<i>DB=EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L25	L21 and (PWM or (pulse width modulation))	3
<input type="checkbox"/>	L24	L20 and (PWM or (pulse width modulation))	3
	<i>DB=PGPB,USPT,EPAB,JPAB,DWPI; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L23	L21 and (PWM or (pulse width modulation))	13
<input type="checkbox"/>	L22	L20 and (PWM or (pulse width modulation))	54
<input type="checkbox"/>	L21	L17 and interference	92
<input type="checkbox"/>	L20	L16 and interference	242
<input type="checkbox"/>	L19	L17 and (PWM or (pulse width modulation))	64
<input type="checkbox"/>	L18	L16 and (PWM or (pulse width modulation))	194
<input type="checkbox"/>	L17	L15 same (frame frequency)	746
<input type="checkbox"/>	L16	L15 and (frame frequency)	1840
<input type="checkbox"/>	L15	(liquid crystal display) or LCD	461209
	<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L14	L13 and (frame frequency)	9
<input type="checkbox"/>	L13	L11 and L12	363

<http://jupiter:9000/bin/cgi-bin/srchhist.pl?state=d9e6nr.30.1&f=ffsearch&userid=mtran7>

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Search History Transcript

2/9/2007

<input type="checkbox"/>	L12	inverter same (PWM or (pulse width modulation))	6410
<input type="checkbox"/>	L11	liquid crystal display	156274
	<i>DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L10	L3 not L5	41
<input type="checkbox"/>	L9	L4 and (PWM or (pulse width modulation))	35
	<i>DB=PGPB; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L8	L3 not L5	46
<input type="checkbox"/>	L7	L4 and (PWM or (pulse width modulation))	2
	<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L6	L3 not L5	87
<input type="checkbox"/>	L5	L4 and (PWM or (pulse width modulation))	37
<input type="checkbox"/>	L4	(345/213)![CCLS]	669
<input type="checkbox"/>	L3	L2 and inverter	99
<input type="checkbox"/>	L2	L1 and (PWM or (pulse width modulation))	344
<input type="checkbox"/>	L1	(345/204 [345/211 [345/212 [345/213)![CCLS]	4520

END OF SEARCH HISTORY

<http://jupiter:9000/bin/cgi-bin/srchhist.pl?state=d9e6nr.30.1&f=ffsearch&userid=mtran7>

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